**25/05/2023**

Plan:

Beginning of my FPGA Synth project.

I am starting in the evening so have a short to-do list to complete.

To do:

* Find github account – DONE
* Cover a simple tutorial – Still going
  + <https://docs.github.com/en/get-started> Going to read through this.
* Set up a demo project – Done
* Briefly log what I’ve done
* Make to do list for tomorrow

Conclusion:

Got a good start on github. Made some demo repos and figured things out. Got stuck on some minor confusions but overall a good use of time. Maybe a bit more to read about for Git but I’ll just create a repo for my project tomorrow and learn as I go. Planned to finish at 23:30 but carried on figuring things out with Git until 23:55, need to be more disciplined on when to stop. Overall… productive!

**26/05/2023:**

Plan:

Tomorrow create a high level plan for the project. FPGA architecture, project timeline, project aims and goals, write about possible risks and uncertainties that could slow down the project, what softwares are we going to use etc. 2 Pages minimum. Remember this is just a simple project to get me going… 2 – 3 weeks max. (Or I guess until I go on holiday).